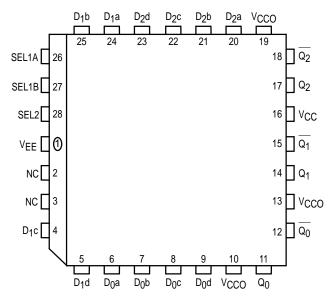
3-Bit 4:1 Multiplexer

The MC10E/100E171 contains three 4:1 multiplexers with differential outputs. Separate Select controls are provided for the leading 2:1 mux pairs (see logic symbol). The three Select inputs control which one of the four data inputs in each case is propagated to the corresponding output.

- 725ps Max. D to Output
- Split Select
- Differential Outputs
- Extended 100E VEE Range of -4.2V to -5.46V
- 75kΩ Input Pulldown Resistors

Pinout: 28-Lead PLCC (Top View)



* All VCC and VCCO pins are tied together on the die.

PIN NAMES

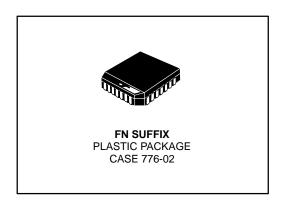
Pin	Function
$D_0x - D_2x$	Data Inputs
SEL1A, SEL1B	First-stage Select Inputs
SEL2	Second-stage Select Input
$Q_0 - Q_2$	True Output
$Q_0 - Q_2$	Inverted Output

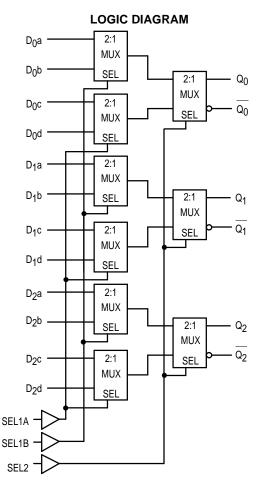
FUNCTION TABLE

Pin	State	Operation
SEL2	Н	Output c/d data
SEL1A	Н	Input d data
SEL1B	Н	Input b data

MC10E171 MC100E171

3-BIT 4:1 MULTIPLEXER







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REV 2

DC CHARACTERISTICS (VEE = VEE(min) to VEE(max); VCC = VCCO = GND)

		0°C		25°C			85°C					
Symbol	Characteristic	min	typ	max	min	typ	max	min	typ	max	Unit	Condition
lН	Input HIGH Current			150			150			150	μΑ	
IEE	Power Supply Current										mA	
	10E		56	67		56	67		56	67		
	100E		56	67		56	67		65	77		

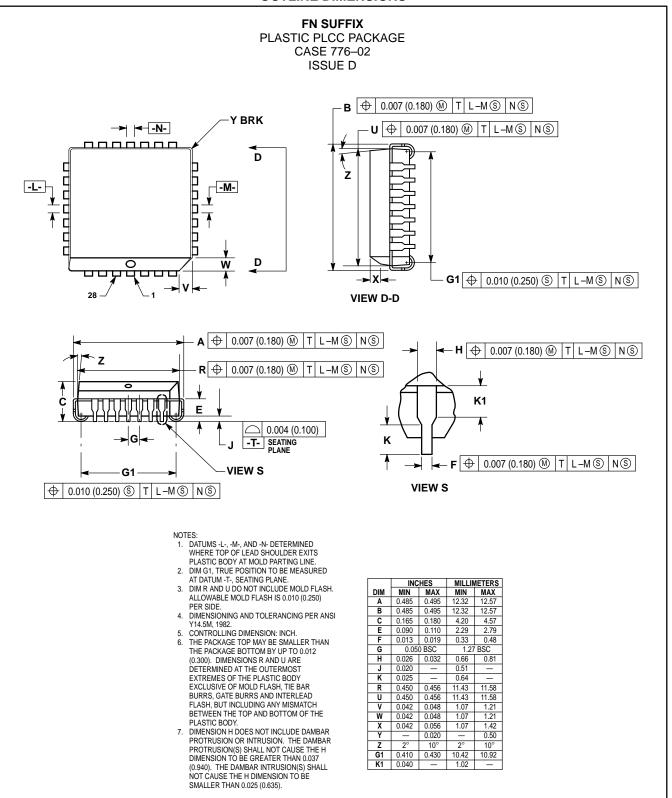
AC CHARACTERISTICS ($V_{EE} = V_{EE}(min)$ to $V_{EE}(max)$; $V_{CC} = V_{CCO} = GND$)

			0°C			25°C			85°C			
Symbol	Characteristic	min	typ	max	min	typ	max	min	typ	max	Unit	Condition
tPLH	Propagation Delay to Output										ps	
tPHL	D	275	480	650	275	480	650	275	480	650		
	SEL1	450	650	850	450	650	850	450	650	850		
	SEL2	350	550	700	350	550	700	350	550	700		
t _{SKEW}	Within-Device Skew										ps	1
	Dnm, Dnm to Qn	1	60			60			60			
	Da, Db, Dc, Dd to Q		40			40			40			
t _r	Rise/Fall Time			·							ps	
t _f	20 - 80%	300	475	650	300	475	650	300	475	650		

^{1.} Within-device skew is defined as identical transitions on similar paths through a device; n = 0,1,2 m = a,b,c,d.

MOTOROLA 2–2

OUTLINE DIMENSIONS



MC10E171 MC100E171

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How to reach us:

USA/EUROPE/Locations Not Listed: Motorola Literature Distribution; P.O. Box 20912; Phoenix, Arizona 85036. 1–800–441–2447 or 602–303–5454

MFAX: RMFAX0@email.sps.mot.com - TOUCHTONE 602-244-6609 INTERNET: http://Design-NET.com

JAPAN: Nippon Motorola Ltd.; Tatsumi–SPD–JLDC, 6F Seibu–Butsuryu–Center, 3–14–2 Tatsumi Koto–Ku, Tokyo 135, Japan. 03–81–3521–8315

ASIA/PACIFIC: Motorola Semiconductors H.K. Ltd.; 8B Tai Ping Industrial Park, 51 Ting Kok Road, Tai Po, N.T., Hong Kong. 852–26629298





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